

Form PTO-1449 LIST OF PRIOR ART CITED BY APPLICANT <i>(Use several sheets if necessary)</i>	ATTORNEY DOCKET NO. AUS920030551US1	SERIAL NO. Not Assigned
	APPLICANT DeWitt, Jr. et al.	
	FILING DATE	GROUP ART UNIT Not Assigned

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EXAMINER INITIAL	DOCUMENT NO.	PUBLICATION DATE	INVENTOR NAME	CLASS/ SUBCLASS	FILING DATE
MPP	AA 5,103,394	Apr. 7, 1992	Blasciak	395/575	Dec. 21, 1989
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EXAMINER INITIAL	DOCUMENT NO.	PUBLICATION DATE	INVENTOR NAME	CLASS/SUBCLASS	FILING DATE
MPP	BB	2002/0129309	Sep. 12, 2002	Floyd et al.	714/724 Dec. 18, 2000
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MPP	BI	JP2000347863	Dec. 15, 2000	Japan	G06F 9/38 X

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MPP	BK Cohen et al., "Hardware-Assisted Characterization of NAS Benchmarks", Cluster Computing, Vol. 4, No. 3, July 2001, pp. 189-196.
MPP	BL Talla et al., "Evaluating Signal Processing and Multimedia Applications on SIMD, VLIW and Super Scalar Architectures", International Conference on Computer Design, Austin, Sept. 17-20, 2000, pp. 163-172.
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RELATED PATENT APPLICATIONS				
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MPP	BP 09/435,069 AT9-99-491	Davidson et al.	Method and Apparatus for Instruction Sampling for Performance Monitoring and Debug	Nov. 4, 1999
MPP	BQ 08/538,071 AUT919950055US1	Gover et al.	Method and System for Selecting and Distinguishing an Event Sequence using an Effective Address in a Processing System	Oct. 2, 1995

DATE CONSIDERED 8/9/07	EXAMINER Mark P. Francis
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U.S. PATENT DOCUMENTS						
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MPF	BR 5,212,794	May 18, 1993	Pettis et al.	395/700	Jun. 1, 1990	
MPF	BS 5,689,712	Nov. 18, 1997	Heisch	395/704	Jul. 27, 1994	

RELATED PATENT APPLICATIONS					
EXAMINER INITIAL	APPLICATION NO/ ATTY. DOCKET NO.	APPLICANT	TITLE	FILING DATE	
MPF	BT Not Assigned AUS920030477US1	DeWitt, Jr. et al.	Method and Apparatus for Counting Instruction Execution and Data Accesses	Sep. 30, 2003	
MPF	BU Not Assigned AUS920030478US1	Levine et al.	Method and Apparatus for Selectively Counting Instructions and Data Accesses	Sep. 30, 2003	
MPF	BV Not Assigned AUS920030479US1	DeWitt, Jr. et al.	Method and Apparatus for Generating Interrupts Upon Execution of Marked Instructions and Upon Access to Marked Memory Locations	Sep. 30, 2003	
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MPF	BX Not Assigned AUS920030481US1	DeWitt, Jr. et al.	Method and Apparatus for Counting Execution of Specific Instructions and Accesses to Specific Data Locations	Sep. 30, 2003	
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MPF	CB Not Assigned AUS920030485US1	Levine et al.	Method and Apparatus to Autonomically Take an Exception on Specified Instructions	Sep. 30, 2003	
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MPF	CD Not Assigned AUS920030487US1	DeWitt, Jr. et al.	Method and Apparatus for Counting Instruction and Memory Location Ranges	Sep. 30, 2003	
MPF	CE Not Assigned AUS920030550US1	DeWitt, Jr. et al.	Autonomic Method and Apparatus for Counting Branch Instructions to Improve Branch Predictions		
MPF	CF Not Assigned AUS920030552US1	DeWitt, Jr. et al.	Autonomic Method and Apparatus for Local Program Code Reorganization Using Branch Count Per Instruction Hardware		

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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT****Complete if Known**

Application Number	10/757,171
Filing Date	01-14-2004
First Named Inventor	DeWitt, Jr. et al.
Examiner Name	Not assigned
Art Unit	2183
Attorney Docket No.	AUS920030551US1

Examiner Initials	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
MPE	AA 5394529	02-28-1995	Brown, III et al.	
MPE	AB 6233679 B1	05-15-2001	Holmberg	
MPE	AC 5051944	09-24-1991	Fetterolf et al.	
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NON PATENT LITERATURE

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Not for submission under 37 CFR 1.99)	Application Number		10757171
	Filing Date		2004-01-14
	First Named Inventor	DeWitt, Jr. et al.	
	Art Unit	2183	
	Examiner Name	Unknown	
	Attorney Docket Number	AUS920030551US1	

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Examiner Initial*	Cite No	Patent Number	Kind Code ¹	Issue Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
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Examiner Initial*	Cite No	Publication Number	Kind Code ¹	Publication Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
MPF	1	20020073406	A1	2002-06-13	Gove	
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	Art Unit	2193	
	Examiner Name	Unassigned	
	Attorney Docket Number	AUS920030551US1	

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Art Unit	2193	
Examiner Name	Unassigned	
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MPF	9	INTEL, "Intel IA-64 Architecture Software Developer's Manual", Revision 1.1, Vol. 4, No. 245320.002, July 2001.	<input type="checkbox"/>
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U.S. PATENTS

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MPF	3	5603004		1997-02-11	Kurpanek et al.	
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MPF	5	6961681	B1	2005-11-01	Choquier et al.	
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MPF	7	6775825	B1	2004-08-10	Grumann et al.	
MPF	8	6601233	B1	2003-07-29	Underwood	

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Attorney Docket Number	AU920030551US1	

MPF	9	6256771	B1	2001-07-03	O'Neil et al.	
MPF	10	6966057	B2	2005-11-15	Lueh	
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MPF	12	6009514	19	1999-12-28	Henzinger et al.	
MPF	13	5544342		1996-08-06	Dean	
MPF	14	6349406	B1	2002-02-19	Levine et al.	

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MPF	1	20030191900	A1	2003-10-09	Hooker	
MPF	2	20040049712	A1	2004-03-11	Betker et al.	
MPF	3	20030225917	A1	2003-12-04	Partamian et al.	

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	Attorney Docket Number	AU920030551US1	

MPPF	4	20030135719	A1	2003-07-17	DeWitt, Jr. et al.	
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